## IN THE UNITED STATES ATENT AND TRADEMARK OFFICE

In re application of:

Atty. Docket: 6161.0104.US

Jae-Bon KOO, et al.

Appl. No.: 10/725,599

Examiner:

WEISS, Howard

Filed: December 3, 2003

Art Unit:

2814

Confirmation No.: 4875

For:

FLAT PANEL DISPLAY WITH THIN FILM TRANSISTOR (TFT)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **STATUS INQUIRY**

Sir:

Please provide us with the status of the above listed patent application which was filed on December 3, 2003.

Respectfully submitted,

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Reg. No. 50,114

Date: February 3, 2006

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(to be used for all correspondence after initial filin		nitial filing)	Examiner Name		WEISS, Howard			
Total Number of Pages in This			Attorney Docket N	umber	6161.0104	I.US		
ENCLOSURES (check all that apply)								
Fee Transmittal Form	☐ Drawing(s			After Allowance Communication to TC				
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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.